

DELTA INFORMATION SYSTEMS, INC.

JANUARY NEWS & UPDATES

[Delta Family of Companies](#)



2018 Events

[Maritime Reconnaissance & Surveillance Technology Conference](#)

AMPEX
January 30-31
Rome, Italy

[AUVSI Unmanned Systems Defense Delta Digital Video](#)

February 6-8
National Harbor, MD
Booth #401

[Singapore Airshow](#)

AMPEX
February 6-11
Singapore
Booth #SME

Read about [AMPEX](#) in [Military Embedded Systems Magazine!](#)



Flight data recorder will go up with NASA's Orion

Delta Digital Video's Orion Data System is a wholly owned subsidiary of Delta Information Systems. The Orion Data System is a contract from space component maker Sierra Nevada Corp. to provide a rugged flight recorder for the Orion (ORV) to the launch vehicle's external cargo system. The Orion Data System, the Model 7500, is a ruggedized, high-speed, multi-channel recorder that will be used for recording during the vehicle's journey to and from the International Space Station (ISS). Under the Orion Data System, Sierra Nevada will use its contracted cargo delivery rights to the ISS between 2019 and 2028.

AmpeX documents describe the T5000 as a standardized, rugged, plug-and-play recorder that integrates the CPU with memory and I/O interfaces such as mini-PCI Express, SATA, USB, Ethernet, and serial ports.

NATO exercises tests at Arpa's Combat System

The U.S. Air Force's Air Force Test and Evaluation Center (AFTEC) conducted a series of Joint Air-to-Air Operations (JA2O) exercises during an event on 2017-05-15.

In use of the event, a U.S. Air Force Combat System was

We Collaborate to Innovate

The [Model 3500 Ethernet Data Recorder](#) has been a collaborative development across the Delta Family of Companies. It has been designed to include AMPEX

Tech Tips: Validate Frame Sync Lock on your Incoming Streams

The [Acroamatics Model 2618P Telemetry Stream Format Validation System](#) supports up to 16



independently programmable channels in a compact 1U rackmount chassis. Each channel has a data/clock input and an independent full function IRIG 106 Chapter 4 frame sync pattern detector with programmable sync strategy (i.e. programmable search/check/lock errors (0 to 15), APC -Auto Polarity control, bit slip window 0 to +/-3). Front panel LEDs provide quick look local lock status. Status of each of the streams is logged and time-tagged in an Excel compatible, downloadable file to document mission data quality performance. A variety of other functions and features are also available.

New Mission Focused Products from AMPEX



miniR700v2 Chapter 10 Solid State Recorder

A P3I update to the original miniR700 with the same SWaP, but dramatic increases in throughput speed. It includes a new CPU, lower cost solid state memories, PCIe High-Speed Interface options, H.264/H.265, backwards compatibility with existing user interfaces, and more.



AMux 1000 Data Acquisition Unit (DAU)

The AMux1000 is a P3I update to the original DAU units, with the same SWaP, increased throughput speed, new CPU, compatibility with existing data interfaces from the miniR700, AMux600, and AMux700 DAU Mux Units, and more.

[Contact](#) your AMPEX Representative for more information.

Video Codecs Now Provide A PCM Interface



A PCM interface is now implemented into [Delta Digital Video's Model 4480S HD/SD Video Codec](#) to ease integration with [telemetry receivers](#) and transmitters. The 4480S is a network appliance designed to compress and decompress High Definition (HD) and Standard Definition (SD) video (HD/SD-SDI, HDMI, RGBHV, RGB, NTSC, PAL) for transmission over IP or synchronous serial networks.

- Can be configured to deliver high-quality video over any network or telemetry datalink
- Compliant with most off-the-shelf video players
- Compact, rugged unit capable of operation in harsh environments
- Optional 1RU, multi-unit rack-mount adapter available

GDP's BERT Products Now Support LDPC

In addition to the diverse set of functions and features currently supported by GDP's [Model 650 Data Link Test Set](#) (i.e. Noise,



Jitter, Acquisition Time, Link Delay Measurement, and Convolutional Encoding), GDP's [BERT products](#) now support the generation of Low Density Parity Check (LDPC) Forward Error Correction (FEC) codes outlined in the new Appendix R of the IRIG106 Telemetry Standard.

recording technology and Acroamatics chassis design assembly for GDP's TMOiP market.



We'd like to recognize **Howard**, Lead Hardware Engineer at Acroamatics, who has taken a critical role in the development of the Model 3500.

Check out what [AMPEX](#) will be featuring at [Singapore Airshow](#)



Over and above the Appendix R codes, the BERT also supports the generation of the CCSDS Near Earth LDPC Codes 7/8th.

LDPC is a recently added function to transmitter and receiver links for new range applications due to their ability to significantly improve link performance. GDP's [telemetry receivers](#) also support all of these LDPC codes on the downlink ([Model 4426](#)).

We Acknowledge Data Privacy All Year Round!



Held annually on **January 28th**, [National Data Privacy Day](#) aims to increase awareness of privacy and data protection issues among consumers, organizations, and government officials. The reliance on computers, networks, control systems, and embedded devices has exponentially increased the cyber-attack surface.

Building on 20 years of high reliability flight test equipment, [AMPEX](#) is launching the **Cyber, Electronic, Analysis, Development & Integration Laboratory (CEADIL)** to further increase reliability and safety for flight and ground control systems. The CEADIL offers far-ranging hardware forensic capabilities that identify and mitigate system vulnerabilities. The CEADIL provides state-of-the-art development, testing, modeling, simulation, and training for the full spectrum of cyber and electronic equipment, applications, and personnel with an emphasis on cyber operations, airborne/ground-based control systems, and associated networks.

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